Precision Low Power FGA™ Voltage References

**ISL60002**

The ISL60002 FGA™ voltage references are very high precision analog voltage references fabricated in Intersil’s proprietary Floating Gate Analog technology and feature low supply voltage operation at ultra-low 350nA operating current.

Additionally, the ISL60002 family features guaranteed initial accuracy as low as ±1.0mV and 20ppm/°C temperature coefficient. The initial accuracy and temperature stability performance of the ISL60002 family, plus the low supply voltage and 350nA power consumption, eliminates the need to compromise thermal stability for reduced power consumption making it an ideal companion to high resolution, low power data conversion systems.

Special Note: Post-assembly x-ray inspection may lead to permanent changes in device output voltage and should be minimized or avoided. For further information, please see “Applications Information” on page 34 and AN1533, “X-Ray Effects on Intersil FGA References”.

**Applications**

- High resolution A/Ds and D/As
- Digital meters
- Bar code scanners
- Mobile communications
- PDA’s and notebooks
- Medical systems

**Features**

- Reference voltages . . . . . . 1.024V, 1.2V, 1.25V, 1.8V, 2.048V, 2.5V, 2.6V, 3.0V and 3.3V
- Absolute initial accuracy options . . . . . . ±1.0mV, ±2.5mV and ±5.0mV
- Supply voltage range
  - ISL60002-10, -11, -12, -18, -20, -25 . . . . . . . . 2.7V to 5.5V
  - ISL60002-26 . . . . . . . . . . . . . . . . . . . . . . . . 2.8V to 5.5V
  - ISL60002-30 . . . . . . . . . . . . . . . . . . . . . . . 3.2V to 5.5V
  - ISL60002-33 . . . . . . . . . . . . . . . . . . . . . . 3.5V to 5.5V
- Ultra-low supply current . . . . . . . . . . . . . . . . . . . . . 350nA typ
- Low 20ppm/°C temperature coefficient
- I\(_{\text{SOURCE}}\) and I\(_{\text{SINK}}\) = 7mA
- I\(_{\text{SOURCE}}\) and I\(_{\text{SINK}}\) = 20mA for ISL60002-33 only
- ESD protection . . . . . . . . . . . . 5500V (Human Body Model)
- Standard 3 Ld SOT-23 packaging
- Operating temperature range
  - ISL60002-10, -11, -12, -18, -20, -25, -26, -30 . . . . . . . . -40°C to +85°C
  - ISL60002-33 . . . . . . . . . . . . . . . . . . . . . . . -40°C to +105°C
- Pb-Free (RoHS Compliant)

**Related Literature**

- AN1494, “Reflow and PC Board Assembly Effects on Intersil FGA References”
- AN1533, “X-Ray Effects on Intersil FGA References”
- AN1843, "ISL60002XXEVAL1Z User’s Guide"

**FIGURE 1. TYPICAL APPLICATION**

VIN = +3.0V

**NOTE:** *Also see Figure 119 on page 35 in Applications Information.*
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ISL60002

Pin Configuration

ISL60002
(3 LD SOT-23)
TOP VIEW

VIN
VOUT
GND

Pin Descriptions

<table>
<thead>
<tr>
<th>PIN #</th>
<th>PIN NAME</th>
<th>DESCRIPTION</th>
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<td>Voltage Reference Output</td>
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<tr>
<td>3</td>
<td>GND</td>
<td>Ground</td>
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Ordering Information

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<th>PART NUMBER (Notes 1, 2, 3)</th>
<th>PART MARKING (Note 4)</th>
<th>VOUT (V)</th>
<th>GRADE</th>
<th>TEMP. RANGE (°C)</th>
<th>PACKAGE (RoHS Compliant)</th>
<th>PKG. DWG. #</th>
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<td>DFB</td>
<td>1.024</td>
<td>±1.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
<td>3 Ld SOT-23</td>
<td>P3.064A</td>
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<td>P3.064A</td>
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<tr>
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### Ordering Information (Continued)

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PART MARKING</th>
<th>V(_{\text{OUT}}) (V)</th>
<th>GRADE</th>
<th>TEMP. RANGE (°C)</th>
<th>PACKAGE (RoHS Compliant)</th>
<th>PKG. DWG. #</th>
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<td>APC</td>
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<td>P3.064A</td>
</tr>
<tr>
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<td>AOP</td>
<td>3.300</td>
<td>±1.0mV, 20ppm/°C</td>
<td>-40 to +105</td>
<td>3 Ld SOT-23</td>
<td>P3.064A</td>
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<tr>
<td>ISL60002BBIH310Z-T7A</td>
<td>DFB</td>
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<td>±1.0mV, 20ppm/°C</td>
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<tr>
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<td>2.500</td>
<td>±1.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
<td>3 Ld SOT-23</td>
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<td>ISL60002DAH333Z-T7A</td>
<td>APC</td>
<td>3.300</td>
<td>±5.0mV, 20ppm/°C</td>
<td>-40 to +105</td>
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<td>P3.064A</td>
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<tr>
<td>ISL60002BIIH310Z-T7A</td>
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<td>1.024</td>
<td>±5.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
<td>3 Ld SOT-23</td>
<td>P3.064A</td>
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<tr>
<td>ISL60002DIH312Z-T7A</td>
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<td>±5.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
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<tr>
<td>ISL60002DIH325Z-T7A</td>
<td>APB</td>
<td>2.500</td>
<td>±5.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
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<td>P3.064A</td>
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<td>ISL60002DIH330Z-T7A</td>
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<td>±5.0mV, 20ppm/°C</td>
<td>-40 to +85</td>
<td>3 Ld SOT-23</td>
<td>P3.064A</td>
</tr>
</tbody>
</table>

**NOTES:**

1. Please refer to TB347 for details on reel specifications.
2. These Intersil Pb-free plastic packaged products employ special Pb-free material sets, molding compounds/die attach materials, and 100% matte tin plate plus anneal (e3 termination finish, which is RoHS compliant and compatible with both SnPb and Pb-free soldering operations). Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.
3. For Moisture Sensitivity Level (MSL), please see device information page for ISL60002. For more information on MSL please see techbrief TB363.
4. The part marking is located on the bottom of the part.
### Absolute Maximum Ratings

<table>
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<tr>
<th>Parameter</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
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<tr>
<td>IOUT</td>
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<td>COUT (Note 10)</td>
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<tr>
<td>TA</td>
<td>-40°C to +85°C</td>
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</table>

**Notes:**

5. Measured with no filtering, distance of 10" from source, intensity set to 55kV and 70mA current, 30s duration. Other exposure levels should be analyzed for Output Voltage drift effects. See “Applications Information” on page 34.

6. $\theta_{JA}$ is measured with the component mounted on a high effective thermal conductivity test board in free air. See Tech Brief TB379 for details.

7. For $\theta_{JC}$, the “case temp” location is taken at the package top center.

8. Post-reflow drift for the ISL60002 devices will range from 100µV to 1.0mV based on experimental results with devices on FR4 double sided boards.

9. Post-assembly X-ray inspection may also lead to permanent changes in device output voltage and should be minimized or avoided. Initial accuracy can change 10mV or more under extreme radiation. Most inspection equipment will not affect the FGA reference voltage, but if X-ray inspection is required, it is advisable to monitor the reference output voltage to verify excessive shift has not occurred.

### Environmental Operating Conditions

<table>
<thead>
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<th>Parameter</th>
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**CAUTION:** Do not operate at or near the maximum ratings listed for extended periods of time. Exposure to such conditions may adversely impact product reliability and result in failures not covered by warranty.

### Electrical Specifications ISL60002-10, $V_{OUT} = 1.024V$

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
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<td>Output Voltage</td>
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<tr>
<td>VOA</td>
<td>NOTES: $V_{OUT}$ Accuracy (Notes 9, 11)</td>
<td>$T_A = +25°C$</td>
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<td>VIN</td>
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### Electrical Specifications ISL60002-11, $V_{OUT} = 1.200V$

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<td>5.5</td>
<td>V</td>
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### Electrical Specifications ISL60002-12, $V_{OUT} = 1.250\, V$

(Additional specifications on page 9, “Common Electrical Specifications”). Operating Conditions: $V_{IN} = 3.0\, V$, $I_{OUT} = 0\, mA$, $C_{OUT} = 0.001\mu F$, $T_A = -40$ to $+85\, ^\circ C$, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +85°C.**

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
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<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
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<tbody>
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<td>Output Voltage</td>
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<td>1.250</td>
<td></td>
<td></td>
<td>V</td>
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<tr>
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<td>$V_{OUT}$ Accuracy (Note 11)</td>
<td>$T_A = +25, ^\circ C$</td>
<td>ISL60002B12</td>
<td>-1.0</td>
<td>+1.0</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>ISL60002C12</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
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<td></td>
<td></td>
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<td>ISL60002D12</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
<td></td>
<td>2.7</td>
<td></td>
<td>5.5</td>
<td>V</td>
</tr>
</tbody>
</table>

### Electrical Specifications ISL60002-18, $V_{OUT} = 1.800\, V$

(Additional specifications on page 9, “Common Electrical Specifications”). Operating Conditions: $V_{IN} = 3.0\, V$, $I_{OUT} = 0\, mA$, $C_{OUT} = 0.001\mu F$, $T_A = -40$ to $+85\, ^\circ C$, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +85°C.**

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
</tr>
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<tbody>
<tr>
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<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{oa}$</td>
<td>$V_{OUT}$ Accuracy (Note 11)</td>
<td>$T_A = +25, ^\circ C$</td>
<td>ISL60002B18</td>
<td>-1.0</td>
<td>+1.0</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>ISL60002C18</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
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<tr>
<td></td>
<td></td>
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<td>ISL60002D18</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
<td></td>
<td>2.7</td>
<td></td>
<td>5.5</td>
<td>V</td>
</tr>
</tbody>
</table>

### Electrical Specifications ISL60002-20, $V_{OUT} = 2.048\, V$

(Additional specifications on page 9, “Common Electrical Specifications”). Operating Conditions: $V_{IN} = 3.0\, V$, $I_{OUT} = 0\, mA$, $C_{OUT} = 0.001\mu F$, $T_A = -40$ to $+85\, ^\circ C$, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +85°C.**

<table>
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<th>TEST CONDITIONS</th>
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<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
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<td></td>
<td>V</td>
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<tr>
<td>$V_{oa}$</td>
<td>$V_{OUT}$ Accuracy (Note 11)</td>
<td>$T_A = +25, ^\circ C$</td>
<td>ISL60002B20</td>
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<td>+1.0</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>ISL60002C20</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>ISL60002D20</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
<td></td>
<td>2.7</td>
<td></td>
<td>5.5</td>
<td>V</td>
</tr>
</tbody>
</table>
## ISL60002

**Electrical Specifications ISL60002-25, VOUT = 2.500V** (Additional specifications on page 9, “Common Electrical Specifications”). Operating Conditions: VIN = 3.0V, IOUT = 0mA, COUT = 0.001µF, TA = -40 to +85°C, unless otherwise specified. **Boldface limits** apply across the operating temperature range, -40°C to +85°C.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
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</thead>
<tbody>
<tr>
<td>VOUT</td>
<td>Output Voltage</td>
<td></td>
<td>2.500</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>VA</td>
<td>VOUT Accuracy (Note 11)</td>
<td>TA = +25°C</td>
<td>ISL60002B25</td>
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<td>+1.0</td>
<td>mV</td>
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<tr>
<td></td>
<td></td>
<td>ISL60002C25</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISL60002D25</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
<td></td>
</tr>
</tbody>
</table>

| V IN   | Input Voltage Range | | 2.7 | 5.5 | V |

**Electrical Specifications ISL60002-26, VOUT = 2.600V** (Additional specifications on page 9, “Common Electrical Specifications”). Operating Conditions: VIN = 3.0V, IOUT = 0mA, COUT = 0.001µF, TA = -40 to +85°C, unless otherwise specified. **Boldface limits** apply across the operating temperature range, -40°C to +85°C.

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>VOUT</td>
<td>Output Voltage</td>
<td></td>
<td>2.600</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>VA</td>
<td>VOUT Accuracy (Note 11)</td>
<td>TA = +25°C</td>
<td>ISL60002B26</td>
<td>-1.0</td>
<td>+1.0</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISL60002C26</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>ISL60002D26</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
<td></td>
</tr>
</tbody>
</table>

| V IN   | Input Voltage Range | | 2.8 | 5.5 | V |
| TC VOUT| Output Voltage Temperature Coefficient (Note 11) | | | 20 | ppm/°C |
| I IN   | Supply Current | | 350 | 900 | nA |
| ΔVOUT/ΔVIN | Line Regulation | +2.8V ≤ V IN ≤ +5.5V | 80 | 350 | µV/V |
| ΔVOUT/ΔIOUT | Load Regulation | 0mA ≤ ISOURCE ≤ 7mA | 25 | 100 | µV/mA |
|            |               | -7mA ≤ ISINK ≤ 0mA | 50 | 250 | µV/mA |
| ΔVOUT/ΔTA | Thermal Hysteresis (Note 12) | ΔTA = +125°C | 100 | | ppm |
| ΔVOUT/Δt | Long Term Stability (Note 13) | TA = +25°C; First 1khrs | 50 | | ppm |
| ISC     | Short Circuit Current (to GND)* | TA = +25°C | 50 | | mA |
| VN      | Output Voltage Noise | 0.1Hz ≤ f ≤ 10Hz | 30 | | µVP-P |
### Electrical Specifications ISL60002-30, $V_{OUT} = 3.000\text{V}$

Operating Conditions: $V_{IN} = 5.0\text{V}$, $I_{OUT} = 0\text{mA}$, $C_{OUT} = 0.001\mu\text{F}$, $T_A = -40$ to $+85\degree\text{C}$, unless otherwise specified. **Boldface limits apply across the operating temperature range, -40°C to +85°C.**

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OUT}$</td>
<td>Output Voltage</td>
<td></td>
<td>3.000</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{OA}$</td>
<td>$V_{OUT}$ Accuracy <em>(Note 11)</em></td>
<td>$T_A = +25\degree\text{C}$</td>
<td>ISL60002B30</td>
<td>-1.0</td>
<td>+1.0</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>ISL60002C30</td>
<td>-2.5</td>
<td>+2.5</td>
<td>mV</td>
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<td></td>
<td></td>
<td>ISL60002D30</td>
<td>-5.0</td>
<td>+5.0</td>
<td>mV</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
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<td>3.2</td>
<td>5.5</td>
<td></td>
<td>V</td>
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<td>TC $V_{OUT}$</td>
<td>Output Voltage Temperature Coefficient <em>(Note 11)</em></td>
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<td>20</td>
<td></td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td>$I_{IN}$</td>
<td>Supply Current</td>
<td></td>
<td>350</td>
<td>900</td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta V_{IN}$</td>
<td>Line Regulation</td>
<td>$+3.2\text{V} \leq V_{IN} \leq +5.5\text{V}$</td>
<td>80</td>
<td>250</td>
<td></td>
<td>µV/V</td>
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<tr>
<td>$\Delta V_{OUT}/\Delta I_{OUT}$</td>
<td>Load Regulation</td>
<td>$0\text{mA} \leq I_{SOURCE} \leq 7\text{mA}$</td>
<td>25</td>
<td>100</td>
<td></td>
<td>µV/µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$-7\text{mA} \leq I_{SINK} \leq 0\text{mA}$</td>
<td>50</td>
<td>150</td>
<td></td>
<td>µV/µA</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta T_A$</td>
<td>Thermal Hysteresis <em>(Note 12)</em></td>
<td>$\Delta T_A = +125\degree\text{C}$</td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta t$</td>
<td>Long Term Stability <em>(Note 13)</em></td>
<td>$T_A = +25\degree\text{C}$; First 1khrs</td>
<td>50</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>$I_{SC}$</td>
<td>Short Circuit Current (to GND)</td>
<td>$T_A = +25\degree\text{C}$</td>
<td>50</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{N}$</td>
<td>Output Voltage Noise</td>
<td>$0.1\text{Hz} \leq f \leq 10\text{Hz}$</td>
<td>30</td>
<td></td>
<td></td>
<td>µV_{P-P}</td>
</tr>
</tbody>
</table>

### Electrical Specifications ISL60002-33, $V_{OUT} = 3.300\text{V}$

Operating Conditions: $V_{IN} = 5.0\text{V}$, $I_{OUT} = 0\text{mA}$, $C_{OUT} = 0.001\mu\text{F}$, $T_A = -40$ to $+105\degree\text{C}$, unless otherwise specified. **Boldface limits apply over the operating temperature range, -40°C to +105°C.**

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNITS</th>
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<tbody>
<tr>
<td>$V_{OUT}$</td>
<td>Output Voltage</td>
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<td>3.300</td>
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<td>V</td>
</tr>
<tr>
<td>$V_{OA}$</td>
<td>$V_{OUT}$ Accuracy <em>(Note 11)</em></td>
<td>$T_A = +25\degree\text{C}$</td>
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<td>1.0</td>
<td>mV</td>
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<tr>
<td></td>
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<td>-2.5</td>
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<td>ISL60002D33</td>
<td>-5.0</td>
<td>5.0</td>
<td>mV</td>
</tr>
<tr>
<td>TC $V_{OUT}$</td>
<td>Output Voltage Temperature Coefficient <em>(Note 11)</em></td>
<td></td>
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<td>ppm/°C</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Input Voltage Range</td>
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<td>3.5</td>
<td>5.5</td>
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<td>V</td>
</tr>
<tr>
<td>$I_{IN}$</td>
<td>Supply Current</td>
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<td>700</td>
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<td>nA</td>
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<tr>
<td>$\Delta V_{OUT}/\Delta V_{IN}$</td>
<td>Line Regulation</td>
<td>$+3.5\text{V} \leq V_{IN} \leq +5.5\text{V}$</td>
<td>80</td>
<td>200</td>
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<td>µV/V</td>
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<tr>
<td>$\Delta V_{OUT}/\Delta I_{OUT}$</td>
<td>Load Regulation</td>
<td>$0\text{mA} \leq I_{SOURCE} \leq 20\text{mA}$</td>
<td>25</td>
<td>100</td>
<td></td>
<td>µV/µA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$-20\text{mA} \leq I_{SINK} \leq 0\text{mA}$</td>
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<td>150</td>
<td></td>
<td>µV/µA</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta T_A$</td>
<td>Thermal Hysteresis <em>(Note 12)</em></td>
<td>$\Delta T_A = +145\degree\text{C}$</td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta t$</td>
<td>Long Term Stability <em>(Note 13)</em></td>
<td>$T_A = +25\degree\text{C}$; First 1khrs</td>
<td>50</td>
<td></td>
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<td>ppm</td>
</tr>
<tr>
<td>$I_{SC}$</td>
<td>Short Circuit Current (to GND)</td>
<td>$T_A = +25\degree\text{C}$</td>
<td>50</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{N}$</td>
<td>Output Voltage Noise</td>
<td>$0.1\text{Hz} \leq f \leq 10\text{Hz}$</td>
<td>30</td>
<td></td>
<td></td>
<td>µV_{P-P}</td>
</tr>
</tbody>
</table>
# Common Electrical Specifications ISL60002-10, -11, -12, -18, -20, and -25

Operating Conditions: $V_{IN} = 3.0\, \text{V}$, $I_{OUT} = 0\, \text{mA}$, $C_{OUT} = 0.001\, \mu\text{F}$, $T_A = -40$ to $+85\, ^\circ\text{C}$, unless otherwise specified. **Boldface limits** apply over the operating temperature range, $-40\, ^\circ\text{C}$ to $+85\, ^\circ\text{C}$.

<table>
<thead>
<tr>
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<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN (Note 10)</th>
<th>TYP</th>
<th>MAX (Note 10)</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>TC $V_{OUT}$</td>
<td>Output Voltage Temperature Coefficient (Note 11)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>ppm/°C</td>
</tr>
<tr>
<td>$I_{IN}$</td>
<td>Supply Current</td>
<td></td>
<td></td>
<td>350</td>
<td>900</td>
<td>nA</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta V_{IN}$</td>
<td>Line Regulation</td>
<td>$+2.7, \text{V} \leq V_{IN} \leq +5.5, \text{V}$</td>
<td>80</td>
<td>250</td>
<td></td>
<td>µV/V</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta I_{OUT}$</td>
<td>Load Regulation</td>
<td>$0, \text{mA} \leq I_{SOURCE} \leq 7, \text{mA}$</td>
<td>25</td>
<td>100</td>
<td></td>
<td>µV/mA</td>
</tr>
<tr>
<td>$\Delta I_{SOURCE}$</td>
<td>$-7, \text{mA} \leq I_{SINK} \leq 0, \text{mA}$</td>
<td></td>
<td>50</td>
<td>150</td>
<td></td>
<td>µV/mA</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta T_A$</td>
<td>Thermal Hysteresis (Note 12)</td>
<td>$\Delta T_A = +125, ^\circ\text{C}$</td>
<td>100</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>$\Delta V_{OUT}/\Delta t$</td>
<td>Long Term Stability (Note 13)</td>
<td>$T_A = +25, ^\circ\text{C}$; First 1khrs</td>
<td>50</td>
<td></td>
<td></td>
<td>ppm</td>
</tr>
<tr>
<td>$I_{SC}$</td>
<td>Short Circuit Current (to GND) (Note 14)</td>
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<td>50</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{IN}$</td>
<td>Output Voltage Noise (Note 15)</td>
<td>$0.1, \text{Hz} \leq f \leq 10, \text{Hz}$</td>
<td>30</td>
<td></td>
<td></td>
<td>µV_{P-P}</td>
</tr>
</tbody>
</table>

**NOTES:**

10. Compliance to datasheet limits is assured by one or more methods: production test, characterization and/or design.

11. Over the specified temperature range. Temperature coefficient is measured by the box method whereby the change in $V_{OUT}$ is divided by the temperature range: $(-40\, ^\circ\text{C} \text{ to } +85\, ^\circ\text{C} = +125\, ^\circ\text{C}$, or $-40\, ^\circ\text{C} \text{ to } +105\, ^\circ\text{C} = +145\, ^\circ\text{C}$ for the ISL60002-33).

12. Thermal Hysteresis is the change in $V_{OUT}$ measured at $T_A = +25\, ^\circ\text{C}$ after temperature cycling over a specified range, $\Delta T_A$. $V_{OUT}$ is read initially at $T_A = +25\, ^\circ\text{C}$ for the device under test. The device is temperature cycled and a second $V_{OUT}$ measurement is taken at $+25\, ^\circ\text{C}$. The difference between the initial $V_{OUT}$ reading and the second $V_{OUT}$ reading is then expressed in ppm. For $\Delta T_A = +125\, ^\circ\text{C}$, the device under is cycled from $+25\, ^\circ\text{C}$ to $+85\, ^\circ\text{C}$ to $-40\, ^\circ\text{C}$ to $+25\, ^\circ\text{C}$, and for $\Delta T_A = +145\, ^\circ\text{C}$, the device under is cycled from $+25\, ^\circ\text{C}$ to $+105\, ^\circ\text{C}$ to $-40\, ^\circ\text{C}$ to $+25\, ^\circ\text{C}$.

13. Long term drift is logarithmic in nature and diminishes over time. Drift after the first 1000 hours will be approximately 10 ppm.

14. Short Circuit Current (to $V_{CC}$) for ISL60002-25 at $V_{IN} = 5.0\, \text{V}$ and $+25\, ^\circ\text{C}$ is typically around 30 mA. Shorting $V_{OUT}$ to $V_{CC}$ is not recommended due to risk of resetting the part.
ISL60002

Typical Performance Characteristic Curves, $V_{OUT} = 1.024V$  \hspace{1cm} $V_{IN} = 3.0V$, $I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified.

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure2.png}
\caption{\textbf{FIGURE 2.} $I_{IN}$ vs $V_{IN}$. 3 UNITS}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure3.png}
\caption{\textbf{FIGURE 3.} $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure4.png}
\caption{\textbf{FIGURE 4.} LINE REGULATION. 3 UNITS}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure5.png}
\caption{\textbf{FIGURE 5.} LINE REGULATION OVER-TEMPERATURE}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{figure6.png}
\caption{\textbf{FIGURE 6.} $V_{OUT}$ vs TEMPERATURE NORMALIZED to $+25^\circ C$}
\end{figure}
Typical Performance Characteristic Curves, \( V_{\text{OUT}} = 1.024\text{V} \), \( V_{\text{IN}} = 3.0\text{V} \).

- FIGURE 7. LINE TRANSIENT RESPONSE, WITH CAPACITIVE LOAD
- FIGURE 8. LINE TRANSIENT RESPONSE
- FIGURE 9. LOAD REGULATION OVER-TEMPERATURE
- FIGURE 10. LOAD TRANSIENT RESPONSE
- FIGURE 11. LOAD TRANSIENT RESPONSE
Typical Performance Characteristic Curves, $V_{OUT} = 1.024V$  
$I_{OUT} = 0mA$, $T_A = +25 ^\circ C$ unless otherwise specified. (Continued)

FIGURE 12. TURN-ON TIME (+25°C)

FIGURE 13. TURN-ON TIME (+25°C)

FIGURE 14. $Z_{OUT}$ vs FREQUENCY
Typical Performance Characteristic Curves, $V_{\text{OUT}} = 1.20\text{V}$  
$V_{\text{IN}} = 3.0\text{V}, I_{\text{OUT}} = 0\text{mA}, T_{\text{A}} = +25\degree \text{C}$ unless otherwise specified.

**FIGURE 15.** $I_{\text{IN}}$ vs $V_{\text{IN}}$, 3 UNITS

**FIGURE 16.** $I_{\text{IN}}$ vs $V_{\text{IN}}$ OVER-TEMPERATURE

**FIGURE 17.** $V_{\text{OUT}}$ vs TEMPERATURE NORMALIZED TO $+25\degree \text{C}$

**FIGURE 18.** LINE REGULATION, 3 UNITS

**FIGURE 19.** LINE REGULATION OVER-TEMPERATURE
Typical Performance Characteristic Curves, $V_{OUT} = 1.20V$
$V_{IN} = 3.0V, I_{OUT} = 0mA$

$T_A = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 20. LINE TRANSIENT RESPONSE**

**FIGURE 21. LINE TRANSIENT RESPONSE WITH CAPACITIVE LOAD**

**FIGURE 22. PSRR vs CAPACITIVE LOAD**

**FIGURE 23. LOAD REGULATION OVER-TEMPERATURE**

**FIGURE 24. LOAD TRANSIENT RESPONSE**

**FIGURE 25. LOAD TRANSIENT RESPONSE**
Typical Performance Characteristic Curves, $V_{OUT} = 1.20V$

$V_{IN} = 3.0V$, $I_{OUT} = 0mA$

$T_A = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 26. TURN-ON TIME (+25°C)**

**FIGURE 27. $Z_{OUT}$ vs FREQUENCY**

**FIGURE 28. $V_{OUT}$ NOISE**
Typical Performance Characteristic Curves, $V_{OUT} = 1.25V$

$I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

**FIGURE 29.** $I_{IN}$ vs $V_{IN}$, 3 UNITS

**FIGURE 30.** $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE

**FIGURE 31.** $V_{OUT}$ vs TEMPERATURE NORMALIZED TO +25°C

**FIGURE 32.** LINE REGULATION, 3 UNITS

**FIGURE 33.** LINE REGULATION OVER-TEMPERATURE
Typical Performance Characteristic Curves, $V_{\text{OUT}} = 1.25\text{V}$ $V_{\text{IN}} = 3.0\text{V}$, $I_{\text{OUT}} = 0\text{mA}$, $T_{A} = +25^\circ\text{C}$ unless otherwise specified. (Continued)

FIGURE 34. LINE TRANSIENT RESPONSE

FIGURE 35. LINE TRANSIENT RESPONSE, WITH CAPACITIVE LOAD

FIGURE 36. PSRR vs CAPACITIVE LOAD

FIGURE 37. LOAD REGULATION

FIGURE 38. LOAD TRANSIENT RESPONSE

FIGURE 39. LOAD TRANSIENT RESPONSE
Typical Performance Characteristic Curves, $V_{OUT} = 1.25V \quad V_{IN} = 3.0V,$ $I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified. (Continued)

FIGURE 40. TURN-ON TIME (+25°C)

FIGURE 41. $Z_{OUT}$ vs FREQUENCY

FIGURE 42. $V_{OUT}$ NOISE
Typical Performance Curves, $V_{OUT} = 1.8V$

$V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

FIGURE 43. $I_{IN}$ vs $V_{IN}$, 3 UNITS

FIGURE 44. $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE

FIGURE 45. LINE REGULATION (3 REPRESENTATIVE UNITS)

FIGURE 46. LINE REGULATION OVER-TEMPERATURE

FIGURE 47. LINE TRANSIENT RESPONSE, WITH CAPACITIVE LOAD

FIGURE 48. LINE TRANSIENT RESPONSE
Typical Performance Curves, $V_{OUT} = 1.8V$

$V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 49. PSRR vs CAPACITIVE LOAD**

- NO LOAD
- 1nF LOAD
- 10nF LOAD
- 100nF LOAD

**FIGURE 50. LOAD REGULATION OVER-TEMPERATURE**

- -40°C
- +25°C
- +85°C

- 500mV/DIV
- 2ms/DIV
- $\Delta I_L = 10mA$
- $\Delta I_L = -10mA$

**FIGURE 51. LOAD TRANSIENT RESPONSE**

- 1ms/DIV
- $\Delta I_L = 50\mu A$
- $\Delta I_L = -50\mu A$

**FIGURE 52. LOAD TRANSIENT RESPONSE**

- 500mV/DIV
- 1ms/DIV
- $\Delta I_L = 10mA$
- $\Delta I_L = -10mA$

**FIGURE 53. TURN-ON TIME (+25°C)**

- 3.2
- 2.8
- 2.4
- 2.0
- 1.6
- 1.2
- 0.8
- 0.4
- 0

**FIGURE 54. TURN-ON TIME (+25°C)**

- 3.2
- 2.8
- 2.4
- 2.0
- 1.6
- 1.2
- 0.8
- 0.4

- $V_{IN}$
- $V_{REF}$

- UNIT 3
- UNIT 2
- UNIT 1

- 0 2 4 6 8 10 12

- TIME (ms)

- 0 2 4 6 8 10 12

- TIME (ms)
Typical Performance Curves, $V_{OUT} = 1.8V$

$V_{IN} = 3.0V, I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified. (Continued)
Typical Performance Curves, $V_{OUT} = 2.048V$

$V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified.

**FIGURE 57.** $I_{IN}$ vs $V_{IN}$ (3 REPRESENTATIVE UNITS)

**FIGURE 58.** $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE

**FIGURE 59.** LINE REGULATION (3 REPRESENTATIVE UNITS)

**FIGURE 60.** LINE REGULATION OVER-TEMPERATURE

**FIGURE 61.** $V_{OUT}$ vs TEMPERATURE NORMALIZED to $+25^\circ C$
Typical Performance Curves, \( V_{\text{OUT}} = 2.048\text{V} \)

\( V_{\text{IN}} = 3.0\text{V}, I_{\text{OUT}} = 0\text{mA}, T_A = +25 ^\circ\text{C} \) unless otherwise specified. (Continued)

**FIGURE 62.** LINE TRANSIENT RESPONSE, WITH CAPACITIVE LOAD

**FIGURE 63.** LINE TRANSIENT RESPONSE

**FIGURE 64.** LOAD REGULATION OVER-TEMPERATURE

**FIGURE 65.** LOAD TRANSIENT RESPONSE

**FIGURE 66.** LOAD TRANSIENT RESPONSE
Typical Performance Curves, $V_{\text{OUT}} = 2.048V$

$V_{\text{IN}} = 3.0V$, $I_{\text{OUT}} = 0mA$, $T_A = +25^\circ C$ unless otherwise specified. (Continued)
Typical Performance Characteristic Curves, $V_{\text{OUT}} = 2.50\text{V}$, $V_{\text{IN}} = 3.0\text{V}$, $I_{\text{OUT}} = 0\text{mA}$, $T_A = +25^\circ\text{C}$ unless otherwise specified.
Typical Performance Characteristic Curves, $V_{OUT} = 2.50V$ $V_{IN} = 3.0V$, $I_{OUT} = 0mA$, $T_{A} = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 75. LINE TRANSIENT RESPONSE**

$C_L = 0nF$

$\Delta V_{IN} = -0.30V$  $\Delta V_{IN} = 0.30V$

**FIGURE 76. LINE TRANSIENT RESPONSE**

$C_L = 1nF$

$\Delta V_{IN} = -0.30V$  $\Delta V_{IN} = 0.30V$

**FIGURE 77. PSRR vs CAPACITIVE LOAD**

$PSRR (dB)$

$V_{OUT} (mV)$

**FIGURE 78. LOAD REGULATION OVER-TEMPERATURE**

$SINKING$  $OUTPUT CURRENT (mA)$  $SOURCING$

$+85^\circ C$  $+25^\circ C$  $-40^\circ C$

$NO LOAD$  $1nF LOAD$  $10nF LOAD$  $100nF LOAD$

**FIGURE 79. LOAD TRANSIENT RESPONSE**

$I_L = -50\mu A$  $I_L = 50\mu A$

**FIGURE 80. LOAD TRANSIENT RESPONSE**

$I_L = -7mA$  $I_L = 7mA$
Typical Performance Characteristic Curves, $V_{\text{OUT}} = 2.50\text{V}$, $V_{\text{IN}} = 3.0\text{V}$, $I_{\text{OUT}} = 0\text{mA}$, $T_{\text{A}} = +25\, ^{\circ}\text{C}$ unless otherwise specified. (Continued)

**Figure 81.** Turn-on Time (+25°C)

**Figure 82.** $Z_{\text{OUT}}$ vs Frequency

**Figure 83.** $V_{\text{OUT}}$ Noise
Typical Performance Characteristic Curves, $V_{OUT} = 3.0V$  
$V_{IN} = 5.0V, I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified.

FIGURE 84. $I_{IN}$ vs $V_{IN}$, 3 UNITS

FIGURE 85. $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE

FIGURE 86. $V_{OUT}$ vs TEMPERATURE NORMALIZED TO $+25^\circ C$

FIGURE 87. LINE REGULATION (3 REPRESENTATIVE UNITS)

FIGURE 88. LINE REGULATION OVER-TEMPERATURE
Typical Performance Characteristic Curves, $V_{OUT} = 3.0V$  $V_{IN} = 5.0V, I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified. (Continued)
Typical Performance Characteristic Curves, $V_{OUT} = 3.0V$

$V_{IN} = 5.0V, I_{OUT} = 0mA, T_A = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 95. LOAD TRANSIENT RESPONSE**

![Graph showing load transient response](image)

**FIGURE 96. LOAD TRANSIENT RESPONSE**

![Graph showing load transient response](image)

**FIGURE 97. TURN-ON TIME (+25°C)**

![Graph showing turn-on time](image)

**FIGURE 98. $Z_{OUT}$ vs FREQUENCY**

![Graph showing $Z_{OUT}$ vs frequency](image)
Typical Performance Characteristic Curves, $V_{OUT} = 3.3V$, $V_{IN} = 5.0V$, $I_{OUT} = 0mA$, $T_a = +25^\circ C$ unless otherwise specified.

**FIGURE 99. $I_{IN}$ vs $V_{IN}$, 3 UNITS**

**FIGURE 100. $I_{IN}$ vs $V_{IN}$ OVER-TEMPERATURE**

**FIGURE 101. $V_{OUT}$ vs TEMPERATURE NORMALIZED TO $+25^\circ C$**

**FIGURE 102. LINE REGULATION, 3 UNITS**

**FIGURE 103. LINE REGULATION OVER-TEMPERATURE**
Typical Performance Characteristic Curves, $V_{OUT} = 3.3V$  
$V_{IN} = 5.0V$, $I_{OUT} = 0mA$, $T_{A} = +25^\circ C$ unless otherwise specified. (Continued)

**FIGURE 104. LINE TRANSIENT RESPONSE**

**FIGURE 105. LINE TRANSIENT RESPONSE**

**FIGURE 106. PSRR vs CAPACITIVE LOAD**

**FIGURE 107. LOAD REGULATION**

**FIGURE 108. LOAD REGULATION OVER-TEMPERATURE**
Typical Performance Characteristic Curves, $V_{OUT} = 3.3V$

$V_{IN} = 5.0V$, $I_{OUT} = 0mA$

$T_A = +25^\circ C$ unless otherwise specified. (Continued)
Applications Information

FGA Technology

The ISL60002 series of voltage references use the floating gate technology to create references with very low drift and supply current. Essentially, the charge stored on a floating gate cell is set precisely in manufacturing. The reference voltage output itself is a buffered version of the floating gate voltage. The resulting reference device has excellent characteristics, which are unique in the industry: very low temperature drift, high initial accuracy, and almost zero supply current. Also, the reference voltage itself is not limited by voltage bandgaps or zener settings, so a wide range of reference voltages can be programmed (standard voltage settings are provided, but customer-specific voltages are available).

The process used for these reference devices is a floating gate CMOS process, and the amplifier circuitry uses CMOS transistors for amplifier and output transistor circuitry. While providing excellent accuracy, there are limitations in output noise level and load regulation due to the MOS device characteristics. These limitations are addressed with circuit techniques discussed in other sections.

Nanopower Operation

Reference devices achieve their highest accuracy when powered up continuously, and after initial stabilization has taken place. This drift can be eliminated by leaving the power on continuously.

The ISL60002 is the first high precision voltage reference with ultra low power consumption that makes it possible to leave power on continuously in battery operated circuits. The ISL60002 consumes extremely low supply current due to the proprietary FGA technology. Supply current at room temperature is typically 350nA, which is 1 to 2 orders of magnitude lower than competitive devices. Application circuits using battery power will benefit greatly from having an accurate, stable reference, which essentially presents no load to the battery.

In particular, battery powered data converter circuits that would normally require the entire circuit to be disabled when not in use can remain powered up between conversions as shown in Figure 117.

Data acquisition circuits providing 12 to 24 bits of accuracy can operate with the reference device continuously biased with no power penalty, providing the highest accuracy and lowest possible long term drift.

Other reference devices consuming higher supply currents will need to be disabled in between conversions to conserve battery capacity. Absolute accuracy will suffer as the device is biased and requires time to settle to its final value, or, may not actually settle to a final value as power on time may be short.

Board Mounting Considerations

For applications requiring the highest accuracy, board mounting location should be reviewed. Placing the device in areas subject to slight twisting can cause degradation of the accuracy of the reference voltage due to die stresses. It is normally best to place the device near the edge of a board, or the shortest side, as the axis of bending is most limited at that location. Obviously mounting the device on flexprint or extremely thin PC material will likewise cause loss of reference accuracy.
**Board Assembly Considerations**

FGA references provide high accuracy and low temperature drift but some PC board assembly precautions are necessary. Normal output voltage shifts of 100µV to 1mV can be expected with Pb-free reflow profiles. Precautions should be taken to avoid excessive heat or extended exposure to high reflow temperatures, which may reduce device initial accuracy.

Post-assembly X-ray inspection may also lead to permanent changes in device output voltage and should be minimized or avoided. If X-ray inspection is required, it is advisable to monitor the reference output voltage to verify excessive shift has not occurred. If large amounts of shift are observed, it is best to add an X-ray shield consisting of thin zinc (300µm) sheeting to allow clear imaging, yet block X-ray energy that affects the FGA reference.

**Special Applications Considerations**

In addition to post-assembly examination, there are also other X-ray sources that may affect the FGA reference long term accuracy. Airport screening machines contain X-rays and will have a cumulative effect on the voltage reference output accuracy. Carry-on luggage screening uses low level X-rays and is not a major source of output voltage shift, however, if a product is expected to pass through that type of screening over 100 times, it may need to consider shielding with copper or aluminum. Checked luggage X-rays are higher intensity and can cause output voltage shift in much fewer passes, thus devices expected to go through those machines should definitely consider shielding. Note that just two layers of 1/2 ounce copper planes will reduce the received dose by over 90%. The leadframe for the device which is on the bottom also provides similar shielding.

If a device is expected to pass through luggage X-ray machines numerous times, it is advised to mount a 2-layer (minimum) PC board on the top, and along with a ground plane underneath will effectively shield it from 50 to 100 passes through the machine. Since these machines vary in X-ray dose delivered, it is difficult to produce an accurate maximum pass recommendation.

**Noise Performance and Reduction**

The output noise voltage in a 0.1Hz to 10Hz bandwidth is typically 30µVP-P. This is shown in the plot in the Typical Performance Curves. The noise measurement is made with a bandpass filter made of a 1 pole high-pass filter with a corner frequency at 0.1Hz and a 2-pole low-pass filter with a corner frequency at 12.6Hz to create a filter with a 9.9Hz bandwidth. Noise in the 10kHz to 1MHz bandwidth is approximately 400µVP-P with no capacitance on the output, as shown in Figure 118. These noise measurements are made with a 2 decade bandpass filter made of a 1 pole high-pass filter with a corner frequency at 1/10 of the center frequency and 1-pole low-pass filter with a corner frequency at 10 times the center frequency. Figure 118 also shows the noise in the 10kHz to 1MHz band can be reduced to about 50µVP-P using a 0.001µF capacitor on the output. Noise in the 1kHz to 100kHz band can be further reduced using a 0.1µF capacitor on the output, but noise in the 1Hz to 100Hz band increases due to instability of the very low power amplifier with a 0.1µF capacitance load. For load capacitances above 0.001µF the noise reduction network shown in Figure 119 is recommended. This network reduces noise significantly over the full bandwidth. As shown in Figure 118, noise is reduced to less than 40µVP-P from 1Hz to 1MHz using this network with a 0.01µF capacitor and a 2kΩ resistor in series with a 10µF capacitor.
**Turn-On Time**

The ISL60002 devices have ultra-low supply current and thus the time to bias up internal circuitry to final values will be longer than with higher power references. Normal turn-on time is typically 7ms. This is shown in Figure 120. Since devices can vary in supply current down to >300nA, turn-on time can last up to about 12ms. Care should be taken in system design to include this delay before measurements or conversions are started.

**Temperature Coefficient**

The limits stated for temperature coefficient (tempco) are governed by the method of measurement. The overwhelming standard for specifying the temperature drift of a reference is to measure the reference voltage at two temperatures, take the total variation, \((V_{HIGH} - V_{LOW})\), and divide by the temperature extremes of measurement \((T_{HIGH} - T_{LOW})\). The result is divided by the nominal reference voltage (at \(T = +25^\circ C\)) and multiplied by \(10^6\) to yield ppm/°C. This is the “Box” method for specifying temperature coefficient.

---

**FIGURE 120. TURN-ON TIME**

![Turn-On Time Graph](image1)

![Temperature Coefficient Graph](image2)
Typical Application Circuits

**FIGURE 121. PRECISION 2.5V 50mA REFERENCE**

- $V_{IN} = 3.0V$
- $V_{OUT} = 2.50V$
- $GND$
- $R = 200\Omega$
- 2N2905
- $0.001\mu F$

**FIGURE 122. 2.5V FULL SCALE LOW-DRIFT 10-BIT ADJUSTABLE VOLTAGE SOURCE**

- $V_{IN}$
- $V_{OUT}$
- ISL60002-25
- $V_{OUT} = 2.50V$
- $GND$
- $V_{CC}$
- $R_H$
- X9119
- $SDA$
- $SCL$
- $V_{SS}$
- $R_L$
- 2-WIRE BUS
- $VOUT$ (BUFFERED)
- $0.1\mu F$
- $10\mu F$
- $0.001\mu F$

**FIGURE 123. KELVIN SENSED LOAD**

- $V_{IN}$
- $V_{OUT}$
- ISL60002-25
- $V_{OUT} = 2.50V$
- $GND$
- $VOUT SENSE$
- LOAD
- $2.7V TO 5.5V$
- $0.1\mu F$
- $10\mu F$
Revision History

The revision history provided is for informational purposes only and is believed to be accurate, but not warranted. Please go to the web to make sure that you have the latest revision.

<table>
<thead>
<tr>
<th>DATE</th>
<th>REVISION</th>
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<tbody>
<tr>
<td>January 8, 2015</td>
<td>FN8082.20</td>
<td>-Updated ordering information table on page 3 by removing withdrawn part numbers: ISL60002BIH320Z, ISL60002BIH325Z, ISL60002CIH320Z, ISL6002DHAH333Z.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>-Changed the y-axis units on Figure 56, on page 21 from 5mV/DIV to 5µV/DIV.</td>
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<tr>
<td></td>
<td></td>
<td>-Added revision history and about Intersil verbiage.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>-Updated POD from P3.064 to P3.064A. Changes are as follows:</td>
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<td></td>
<td>Detail A changes:</td>
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<td></td>
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<td>0.085 - 0.19 to 0.13 ±0.05</td>
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<td></td>
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<td>Removed 0.25 above Gauge Plane</td>
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<td>0.38±0.10 to 0.31 ±0.10</td>
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<td>Side View changes:</td>
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<td></td>
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<td>0.95±0.07 to 0.91 ±0.03</td>
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Package Outline Drawing

P3.064A
3 LEAD SMALL OUTLINE TRANSISTOR PLASTIC PACKAGE (SOT23-3)
Rev 0, 7/14

Dimensions are in millimeters. Dimensions in ( ) for Reference Only.

2. Reference JEDEC TO-236.
3. Dimension does not include interlead flash or protrusions. Interlead flash or protrusions shall not exceed 0.25mm per side.

Footlength is measured at reference to gauge plane.

NOTES: